

Abstract

A plasma display panel in which a plurality of pairs of display electrodes extending in a row direction are aligned on a surface of a first substrate, a plurality of address electrodes extending in a column direction are disposed in a stripe pattern on a surface of a second substrate, the first and second substrates are disposed opposite each other so that the pairs of display electrodes and the address electrodes cross over sandwiching discharge space therebetween, and a discharge cell is formed corresponding to each crossover portion. The pairs of display electrodes are composed of a metallic material, each display electrode of each pair of display electrodes includes a base part extending in the row direction and a plurality of opposing parts extending from the base part into a discharge interval between the each pair of display electrodes. In each discharge cell, at least two discharge starting gaps are formed, each discharge starting gap existing between opposing parts that respectively belong each of the pair display electrodes and being at least partially over the address electrode. Discharge space exists between the each discharge starting gap and the address electrode, and peaks in electric field intensity are formed at each of the opposing parts.